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APPLICATION NO.	F	TILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/023,455		12/17/2001	Takao Ohnishi	796 007	2269
25191	7590	04/18/2006		EXAMINER	
BURR & BROWN			LUDLOW, JAN M		
PO BOX 7068 SYRACUSE, NY 13261-7068				ART UNIT	PAPER NUMBER
				1743	
				DATE MAILED: 04/18/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

				1			
		Application No.	Applicant(s)	7			
		10/023,455	OHNISHI ET AL.				
	Office Action Summary	Examiner	Art Unit .	_			
		Jan M. Ludiow	1743				
Period fo	<ul> <li>The MAILING DATE of this communication apport Reply</li> </ul>	pears on the cover sheet with the o	orrespondence address –				
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATE OF THE OF THE MAILING DATE OF THE MAILING DATE OF THE MAILING DATE OF T	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tir will apply and will expire SIX (6) MONTHS from to cause the application to become ABANDONE	N. mely filed the mailing date of this communication. ED (35 U.S.C. § 133).				
Status							
1)⊠	Responsive to communication(s) filed on <u>03 Fe</u>	ebruary 2006.					
2a) <u></u> ☐	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.						
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Disposit	ion of Claims		,				
4)⊠	Claim(s) <u>1,2,4-8 and 10-13</u> is/are pending in the	ne application.					
/	4a) Of the above claim(s) 13 is/are withdrawn f						
5)□	Claim(s) is/are allowed.						
6)⊠	Claim(s) <u>1,2,4-8 and 10-12</u> is/are rejected.						
7)	Claim(s) is/are objected to.						
8)□	Claim(s) are subject to restriction and/o	or election requirement.	•				
Applicat	ion Papers						
9)	The specification is objected to by the Examine	er.					
•	The drawing(s) filed on 30 April 2002 is/are: a)		by the Examiner.				
	Applicant may not request that any objection to the						
	Replacement drawing sheet(s) including the correct	tion is required if the drawing(s) is ob	ojected to. See 37 CFR 1.121(d).				
11)	The oath or declaration is objected to by the Ex	kaminer. Note the attached Office	Action or form PTO-152.				
Priority	under 35 U.S.C. § 119						
12)🛛	Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a	a)-(d) or (f).				
-	⊠ All b) Some * c) None of:						
	1. Certified copies of the priority documents have been received.						
	2. Certified copies of the priority documents have been received in Application No						
	3. Copies of the certified copies of the prior	rity documents have been receiv	ed in this National Stage				
	application from the International Burea	· · · · · · · · · · · · · · · · · · ·	•				
* ;	See the attached detailed Office action for a list	of the certified copies not receiv	ed.				
A44.a.b	nt/c)						
Attachmei	nt(s) ce of References Cited (PTO-892)	4) Interview Summar	v (PTO-413)				
2) 🔲 Noti	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail D	Date				
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date	5) Notice of Informal 6) Other:	Patent Application (PTO-152)				

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1. Applicant's election with traverse of Group I in the reply filed on February 3, 2006 is acknowledged. The traversal is on the ground(s) that there is no burden in searching the additional invention. This is not found persuasive because the inventions require different search terms in electronic searching, the concomitant analysis of different references and different issues of patentability.

The requirement is still deemed proper and is therefore made FINAL.

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:
  - 1. Determining the scope and contents of the prior art.
  - 2. Ascertaining the differences between the prior art and the claims at issue.
  - 3. Resolving the level of ordinary skill in the pertinent art.
  - 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 3. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to

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consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

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4. Claims 1-2, 5-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ogura (6592819) in view of WO 99/22867 (hereafter WO).

Ogura teaches a prior art method of forming an array by providing a module 30 with a two dimensional array of spotting units 31 used to produce an array on a single substrate. See, e.g., Figures 3-4.

Ogura fails to teach or suggest simultaneous spotting on plural substrates.

WO teaches a method of making arrays on plural array regions by jetting (e.g., p. 4, line 32) from modules 76, 80, each having plural jetting heads 38, 40, wherein the respective "supports" are the rows of rectangular areas shown (Figure 5). Deposition is in "parallel" (p. 5, line 26), which Webster's defines as relating to tasks performed simultaneously (p. 853, <sup>2</sup>parallel, definition 2c).

It would have been obvious to one of ordinary skill to provide plural modules with, e.g., 2x2 injection unit arrays over plural substrates in the prior art invention disclosed by Ogura in order to simultaneously spot plural substrates as taught by WO. It would have been further obvious to use ink jet heads in place of spotting pins as taught by WO.

5. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ogura and WO as applied to claims above, and further in view of Gamble and/or Hirota.

WO fails to teach a piezoelectric ink jet device of the claimed structure.

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Gamble (Figs. 2-3) and/or Hirota (Fig. 2) teach piezoelectric ink jet devices for producing arrays.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to use the ink jet device of Gamble and/or Hirota in the method of Ogura and WO in order to provide a known ink jet device for forming arrays for its known purpose, the use of an ink jet being taught by WO.

6. Claims 7-8, 11-12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ogura and WO as applied to claims above, and further in view of Bass.

WO fails to teach cutting the substrate to form separate chips.

Bass teaches a multiple ejector method for making arrays similar to that of WO.

After formation of plural arrays, the substrate is cut to make separate chips (Fig. 6).

It would have been obvious to cut the substrate(s) of Ogura and WO in order to form separate chips as taught by Bass.

7. Claims 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ogura and WO and Hirota and/or Gamble as applied to claims above, and further in view of Bass.

WO fails to teach cutting the substrate to form separate chips.

Bass teaches a multiple ejector method for making arrays similar to that of WO.

After formation of plural arrays, the substrate is cut to make separate chips (Fig. 6).

It would have been obvious to cut the substrate(s) of Ogura and WO in order to form separate chips as taught by Bass.

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8. Applicant's arguments with respect to claims above have been considered but are most in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jan M. Ludlow whose telephone number is (571) 272-1260. The examiner can normally be reached on Monday-Thursday, 11:30 am - 8:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jill A. Warden can be reached on (571) 272-1267. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jan M. Ludlow Primary Examiner Art Unit 1743

Markhall.

Jml April 17, 2006